

UNIT - IV

10. Interface 64K RAM & 32K ROM with 8086 starting address ? RAM must be 00000H & ending address of ROM must be FFFFFH. Draw interfacing diagram, memory map & decoder table. 10
11. List & explain the activity performed by DMA controller when its \overline{DRS} pin is activated. 10
12. Write note on Trouble shooting memory module. 10

UNIT - V

13. Explain in detail pins QS_0 & QS_1 of 8086. Also explain the significance of LOCK signal in loosely coupled configuration. 10
14. Draw & explain the architecture of NOP 8087. 10
15. Write a program in 8087 assembly language to. 10
- i) Compute hypotenuse side of right angle triangle.
 - ii) Compute area of circle.