Total number of printed pages - 7

B. Tech

PECS 3404

Seventh Semester Examination - 2008

COMPILER DESIGN

Full Marks - 70

Time - 3 Hours

Answer Question No. 1 which is compulsory and any five from the rest.

The figures in the right-hand margin indicate marks.

Answer the following questions :

2×10

- (a) What is the front-end of a compiler?
- (b) What are the error recovery actions in a lexical analyzer?
- (c) What are the goals of error handler in a parser?

P.T.O.

- (d) What do you mean postfix translations?
- (e) Explain, why a common approach are being used to design the Lexical analyzer for different compilers?
- (f) What is basic block in a code segment?
- (g) Why do most compilers contain symbols tables?
- (h) What is the need of attributed grammar and L-attributed grammar in semantic analysis?
- Suggest a efficient data structure to implement LR parsing table.
- (j) Define token and lexeme. Find out the tokens from the following code segment.

```
main()
{

int a = 5;

int b[11];

while (a <= 5)

b[a] = 3 * a;
}
```

(a) Describe the basic differences between top-down and bottom-up parsing. If you had to write a parser by hand which approach would you use? If you had to write a parser for a relatively complex language, which approach would you use?

(b) Show that the following is an SLR (1) grammar.

$$e \rightarrow e \ ADD \ t \mid t$$
 $t \rightarrow t \ MULf \mid f$ 
 $f \rightarrow VAR$ 

3. (a) What is the objective of intermediate code generation? Write quadruples, triples and indirect triples for the following expression.

$$-(a+b)+(c+d)-(a+b+c)$$

(b) Write the role of a error detector in compilation process. Discuss different errors in Lexical-Phase.

PECS 3404

2

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PECS 3404

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P.T.O.

4. (a) Explain the difference between LALR parsing table and canonical LR parsing table?

- (b) Write the algorithm to construct a canonical LR parsing table for the augment grammar.

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- 5. (a) Consider the following context-free grammar. Where S is the start symbol.

$$S \rightarrow (A)$$

$$A \rightarrow a$$

Construct an NFA whose sets are the LR(0) items of the given grammar. Show that the canonical collection of LR(0) items for the grammar is the same as the states of equivalent DFA.

PECS 3404

4

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- (b) Discuss the importance of symbol table in compiler design. How is the symbol table manipulated during semantic analysis phase of compilation?
- (a) What are the issues in the design of the code generator? Explain.
  - (b) What is DAG? Write an algorithm to construct DAG from the block of three address code. Explain the process with a relevant example.
  - (a) What is a three address code? What are its types? How it is implemented? Generator three address code for the following code segment.

PECS 3404

2

P.T.O.

- (b) Discuss the structure of a symbol table.Explain how the symbol table is created for a block structured language.
- 8. (a) Write down the necessary algorithms and define FIRST and FOLLOW. 5
  - (b) Let the Target machine is a byte addressable machine with four bytes to a word and n general-purpose registers, R<sub>0</sub>, R<sub>1</sub>, ..., R<sub>n-1</sub>. It operates on two address instruction of the form op source, destination; The op-codes are MOV, ADD, SUB.

Generate code for the following C statements for a target machine described above. Assume that three registers are available.

PECS 3404

6

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- (i) x = f(a) + f(a) + f(a)
- (ii) a[i] = b[j]
- (iii) a[i] = b[c[i]]
- (iv) a[i] = a[i] + b[j].

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PECS 3404

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