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B.TECH. DEGREE EXAMINATION, MAY/JUNE 2009

Eighth Semester

Branch: Applied Electronics and Instrumentation Engineering

COMPUTERISED PROCESS CONTROL (A)

(Regular/Supplementary)

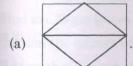
ime: Three Hours

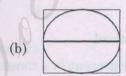
Maximum: 100 Marks

Part A

Answer all questions.

- 1. Give the basic architecture of PLA.
- 2. Explain te operation of PAL.
- 3. Differentiate between combinational logic controller and sequential logic controller.
- 4. What are the features of programmable logic controller?
- 5. Explain the following flow sheet symbols:





- 6. Explain about the remote terminal units.
- 7. Write a note on the integration of DCS with PLC.
- 8. What are the components of fiber optic communication link?
- 9. What are the sources of noise?
- 10. Explain the uses of network protocols.

 $(10 \times 4 = 40 \text{ marks})$

Part B

1. (a) Differentiate between PLA and PAL.

(4 marks)

(b) Discuss the various applications of PLA.

(8 marks)

Or

2. Explain, in detail, the design and applications of PAL.

(12 marks)

Turn over

- (b) Write a note on microprocessor based PLCs. (6 marks
- (a) With a suitable example, explain the design of a logic controller using programmable logic device.
- (8 marks (b) Write a note on PLC programming languages. (4 marks
- Describe the guideline for estimating the total cost of distributed of control system.

With a block diagram, explain the components of a CRT display.

- (a) Explain the various types of tasks performed by supervisory computers. (b) Write a note on TOP.

- Or
- 18. (a) What are the generic features of DCS?
 - List the advantages of field buses over hard-wired installation. (c) Discuss about the integration of DCS with computers.
- Discuss, in detail, about any two types of printers. 19.
- - Or
 - (a) Explain Allen-Bradlley protocol.
 - (b) What are the types of noise present in a communication system.

(12 marks

(12 marks

(12 marks

(7 marks

(5 marks

(4 marks

(4 marks

(4 marks

(8 marks (4 marks