

## **RB-3987-88**

### B. Sc. (IT) (Sem. IV) Examination April / May - 2010

# Microprocessor & Assembly Language

(Old Course)

Time: 3 Hours] [Total Marks: 70

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નીચે દર્શાવેલ 🚁 નિશાનીવાળી વિગતો ઉત્તરવહી પર અવશ્ય લખવી. Fillup strictly the details of 🚁 signs on your answer book. Name of the Examination :	Seat No.:
B. Sc. (IT) (Sem. 4) Name of the Subject :	
◆ Microprocessor & Assembly Language (Old)	
Subject Code No.: 3 9 8 7 Section No. (1, 2,): 1	Student's Signature

- (2) Write section-I and Section-II in **separate** sheets (with different subject code).
- (3) Draw the figure and give example whenever necessary.
- 1 Answer following questions:

10

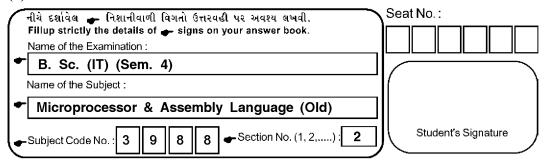
- (1) Explain following instructions (any **five**)
  - (a) MOV DL, F3 h
  - (b) ROL BL, CL
  - (c) SUB CL, DL
  - (d) DAA
  - (e) OR,DX,CX
  - (f) ADDC AL, BL
- (2) Write the addressing modes of the 8086 microprocessor.
- 2 Answer the following questions in detail: (any three) 15
  - (1) Explain the FLAG register of 8086
  - (2) Explain (i) Linker (ii) Assembler
  - (3) Compare 80386 and 80486
  - (4) Write the function reigster: ES,BP,IP,DI,CS,BP
- 3 Briefly explain 8086 architecture with the help of block diagram.

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- (2) Write section-I and Section-II in **separate** sheet (with different subject code).
- (3) Draw the figure and give example whenever necessary.
- 4 Answer the following questions.

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- (1) Explain the function of the 8086 pins:
  - (a) NMI
  - (b) +VCC
  - (c) A16-A19
  - (d) AD0-AD15
  - (e) +BHE
  - (f) GND
  - (2) Describe Assembler Directives
    - (a) DB
    - (b) DW
    - (c) SEGMENT
    - (d) ENDP
- 5 Answer following questions in detail: (any three)

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- (1) Explain block diagram of programmable interrupt controller ic. 8259.
- (2) Explain the block programmable peripheral interface 8255
- (3) What is interrupt vector table decribe TYPE-2, TYPE-0 INTERRUPT
- (4) Explain IDE, SCSI, VESA
- 6 Write program to calculate average of 10 numbers and store its result in data segment memory

OR

6 Write program to arrange given 10 numbers in descending order and store result in data segment memory.

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