

Register Number :

Name of the Candidate :

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B.E. DEGREE EXAMINATION, 2008

(COMPUTER SCIENCE AND ENGINEERING)

(FOURTH SEMESTER)

COEC – 406 / ITEC – 405.

COMPUTER ARCHITECTURE.

(Common with Information Technology)

May]

[Time : 3 Hours

Maximum : 60 Marks

Answer any ONE full question from each unit.

UNIT – I

1. Discuss the evolution of computers in detail. (12)
2. (a) With a neat sketch, explain the operation of a computer. (8)
(b) Discuss the use of interconnection structures. (4)

Turn over

UNIT – II

3. Discuss in detail DRAM organization. (12)
4. (a) Define disk access time. (2)
(b) Discuss in detail about RAID. (10)

UNIT - III

5. (a) How the instructions are represented ? (3)
(b) Explain the different types of operations in the machine. (9)
6. Compare Pentium addressing modes with power PC addressing modes. (12)

UNIT – IV

7. Describe about RISC pipeling. (12)
8. Differentiate RISC from CISC. Identify the characteristics of RISC. (12)

UNIT – V

9. (a) List the advantages and disadvantages of micro-programmed control unit. (6)
(b) Enumerate different types of micro instructions formats. (6)

10. (a) Write short notes on micro instruction encoding. (6)
(b) With an example, explain the function of the control unit. (6)